

## Electronic Patent Application Fee Transmittal

<b>Application Number:</b>	10814968			
<b>Filing Date:</b>	31-Mar-2004			
<b>Title of Invention:</b>	Integrated circuit having a memory cell array capable of simultaneously performing a data read operation and a data write operation			
<b>First Named Inventor/Applicant Name:</b>	Kyo-Min Sohn			
<b>Filer:</b>	Frank Chau/marg dougan			
<b>Attorney Docket Number:</b>	8021-225JHM/SS18860US			
Filed as Large Entity				
<b>Utility Filing Fees</b>				
Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
<b>Basic Filing:</b>				
<b>Pages:</b>				
<b>Claims:</b>				
<b>Miscellaneous-Filing:</b>				
<b>Petition:</b>				
<b>Patent-Appeals-and-Interference:</b>				
<b>Post-Allowance-and-Post-Issuance:</b>				
<b>Extension-of-Time:</b>				
Extension - 3 months with \$0 paid	1253	1	1050	1050

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
<b>Miscellaneous:</b>				
<b>Total in USD (\$)</b>				<b>1050</b>